

**YASHWANTRAO CHAVAN COLLEGE OF SCIENCE,  
KARAD**

**Department of Electronics  
STUDENT SEMINAR B.SC.III  
Academic Year 2025-26**

**INDEX**

Date: 14/03/2026

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Signature

Head of Department

**HEAD**

Department of Electronics

Yashwantrao Chavan College of Science,  
Karad


  
Signature  
Principal  
**Principal**

Yashwantrao Chavan College of Science, Karad



# YASHWANTRAO CHAVAN COLLEGE OF SCIENCE, KARAD

## ACTIVITY REPORT

Name of the Department: Electronics		Academic Year – 2025-26	
		Date: 14/03/2026	
Name of the activity	B.Sc. III Electronics Students Seminar		
Purpose of Program	1)To improve students communication and presentation skill. 2)To build confidence while speaking in front of others 3)To develop thinking and idea sharing ability and increase knowledge about different topics.		
No. of Students Participated	17		
No. of Teachers Participated	05		
Program outcomes	1)It helps to students to gain new knowledge & encourage them to think creatively. 2)Increase students confidence, interest and learning new ideas. 3)Students were able to present topics clearly and confidently. 4)Students learned how to prepare and organize content properly.		
Program Photo			

  
Signature

Teacher incharge

  
Signature

Head of Department

Department of Electronics

Yashwantrao Chavan College of Science, Karad

  
Signature

IQAC coordinator

  
Signature

Principal

Principal



## Activity Photos

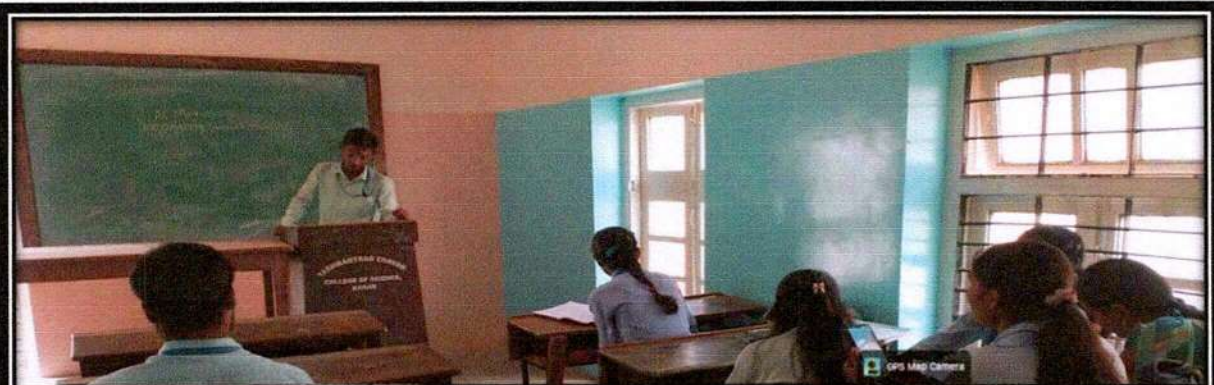


**Vanvadi, Maharashtra, India** 

856q+7gr, Vanvadi, Maharashtra 415124, India

Lat 17.310003° Long 74.18922°

Saturday, 14/03/2026 04:06 PM GMT +05:30



**Karad, Maharashtra, India** 

Yc College Of Science, 1, Near V C Collage, Tarangan Wishaw  
Colony, Vidyanagar, Karad, Maharashtra 415124, India

Lat 17.309946° Long 74.189269°

Saturday, 14/03/2026 04:17 PM GMT +05:30



# YASHWANTRAO CHAVAN COLLEGE OF SCIENCE KARAD

## DEPARTMENT OF ELECTRONICS

### Seminar Attendance

### B.Sc.III

Academic Year 2025 -26

Sr.No.	Roll No.	Name of student	Sign.
1	3006	Sutar Praveen Ratan	
2	3007	Babar Aishwarya Vijay	A.V. Babar
3	3008	Chavan Sohan Sunil	Absent
4	3009	Dixit Akshay Annaso	
5	3010	Kadam Mayuri Santosh	
6	3011	Kadam Sachin Shivaji	
7	3012	Kolekar Vardhan Deepak	
8	3013	Nadageri Laxman Maryappa	
9	3014	Patel Jahid Shahabuddin	
10	3015	Patil Somesh Sanjay	
11	3016	Phadtare Shubham Dhanaji	
12	3017	Dhumal Shravni Kishor	
13	3018	Chavan Pratiksha Sampat	
14	3019	Shinde Atharv Subhash	
15	3020	Ghare Raviraj Vikas	
16	3021	Shinde Renuka Anil	
17	3022	Nikam Rutuja Sachin	R.S.Nikam
18	3023	Mandave Sakshi Santosh	

Teacher incharge



H.O.D.  
HEAD

Department of Electronics  
Yashwantrao Chavan College of Science,  
Karad

# YASHWANTRAO CHAVAN COLLEGE OF SCIENCE KARAD

## DEPARTMENT OF ELECTRONICS

### Seminar Report (Title)

### B.Sc.III

Academic Year 2025 -26

Sr.No.	Roll No.	Name of student	Seminar Title
1	3006	Sutar Praveen Ratan	Robotics
2	3007	Babar Aishwarya Vijay	Basic concept of Lasers.
3	3008	Chavan Sohan Sunil	Absent
4	3009	Dixit Akshay Annaso	Engine managment system.
5	3010	Kadam Mayuri Santosh	Engine managment system
6	3011	Kadam Sachin Shivaji	PD control system
7	3012	Kolekar Vardhan Deepak	I Controller
8	3013	Nadageri Laxman Maryappa	Robotics
9	3014	Patel Jahid Shahabuddin	Robotics
10	3015	Patil Somesh Sanjay	Phototransistor
11	3016	Phadtare Shubham Dhanaji	Robot
12	3017	Dhumal Shravni Kishor	Dhruv 64
13	3018	Chavan Pratiksha Sampat	Robot
14	3019	Shinde Atharv Subhash	optical fiber.
15	3020	Ghare Raviraj Vikas	PI control
16	3021	Shinde Renuka Anil	Reset circuit
17	3022	Nikam Rutuja Sachin	LED
18	3023	Mandave Sakshi Santosh	PID control system



Teacher incharge




HOD  
HEAD

Department of Electronics  
Yashwantrao Chavan College of Science  
Karad

# Yashwantrao Chavan College of Science Karad

## Department of Electronics


### Seminar Report

Names:- Dhumal Shravani Kishor

Class:- Bsc III

Roll No:- 3017

PRN NO:- 2023061457

Seminar on:-  Dhruv 64



Teacher  
Incharge

Examiner

Head of  
Department

# Dhruv 64

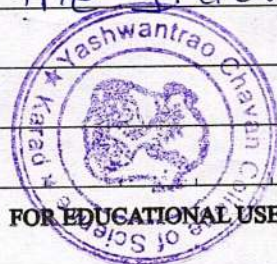
- DHRUV64, India's first homegrown 1.0 GHz, 64-bit, dual-core microprocessor, strengthens the indigenous processor pipeline
- National Programmes like Digital India RISC-V give India the support needed to design, test and prototype indigenous chips, including DHRUV64
- After DHRUV64 success, the next-generation Dhanush and Dhanush+ processors are now under development.



# Introduction

India has achieved a significant milestone in its semiconductor journey with the launch of DHRUV64. It is a fully indigenous microprocessor developed by the Centre for Development of Advanced Computing (C-DAC) under the microprocessor Development program (MPP). DHRUV64 provides the nation a reliable, homegrown processor technology. It is capable of supporting strategic and commercial applications. It marks a major advancement in India's pursuit of self-reliance in advanced chip design.

DHRUV64 is built with modern architectural features. It delivers higher efficiency, enhanced multitasking capability and enables seamless integration with a wide range of external hardware systems. The processor's modern fabrication leverages technologies used for high-performance chips. This makes DHRUV64 suitable for sectors such as 5G infrastructure, automotive systems, consumption and the Internet of Things (IoT).



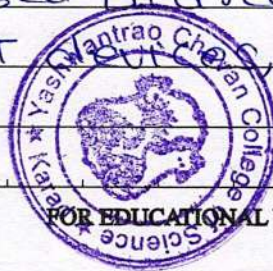
# DHRUV64'S Impact on India's R&D and Innovation

- DHRUV64's provides a homegrown microprocessor technology designed for startups, academia, and industry to build, test, and scale indigenous computing products without relying on foreign processor.
- DHRUV64 supports prototype development for new system architectures at lower cost.
- India already has 20% of world's chip design engineers. DHRUV64 further helps in building a strong pipeline of skilled semiconductor chip professionals.
- The success of DHRUV64 accelerates the roadmap for Dhanush and Dhanush+ processors. They are now under development phase.



# Advantages

- It reduced dependence on foreign, licensed, and costly chips, enhanced security for critical infrastructure, and high-performance capabilities for 5G, IOT and industrial automation.
- High processing speed  
Dhruv-64 is a 64-bit microprocessor, so it can process large amounts of data faster than 32-bit processors.
- Better performance  
It supports advanced computing tasks and provides improved system performance for modern applications.
- Secure Architecture  
It includes security features that help protect data and system operations.
- Suitable for Modern Technologies -  
It can be used in areas like AI systems, robotics, IOT devices, and smart electronics.



Indigenous Development:

The Dhruv-64 processor is part of India's effort to develop its own semiconductor technology, reducing dependence of on foreign processors



# Applications

## 1. Embedded systems :

Dhruv-64 can be used in embedded systems for controlling electronic devices and automated machines.

## 2. Artificial Intelligence systems

It supports AI applications such as machine learning and intelligent data processing.

## 3. Internet of Things (IOT) devices -

The processor can be used in smart home devices, sensors, and connected electronic systems.

## 4. Robotics :

Dhruv-64 can control robotic systems and automated manufacturing machines.

## 5. Consumer Electronics -

It can be used in electronic devices like smart gadgets, digital systems, and other modern electronics.



## 6. Research an Education -

Universities and research labs can be used ~~it~~ for studying processor architecture and advanced computing systems.

## 7. Defense and security systems.

It can be applied in secure computing and defense-related technologies.



# Rollout of DHRUV64 and India's Digital India RISC-V (DIR-V) Progress

The government of India. launched the Digital India RISC-V (DIR-V) Programme to advance the vision of Atmanirbhar Bharat. It aims to establish India as a global hub for Electronics system Design and Manufacturing (ESDM). The initiative develops a complete portfolio of RISC-V-based microprocessors. These processors will power applications across industry, strategic sectors and consumer technologies.



**Yashwantrao Chavan College of Science, Karad**

**Department of Electronics**

**Feedback form**

Name of the Activity – Student Seminar

Roll No :- 3023

Class : B.Sc-III

		Excellent (5)	Very Good (4)	Good (3)	Satisfactory (2)	Poor (1)
1	How would you rate the seminar's organization	✓				
2	How would you rate the Knowledge	✓				
3	Execution of Seminar	✓				
4	Providing the facilities ICT, books, hardware tools etc. during seminar & seminar preparation by department	✓				
5	Support by department during seminar	✓				

Any Other suggestion if any .....

Name of the Students Mandave Sakshi Santosh

Date : 12/03/2026

Sign : Mandave

**Yashwantrao Chavan College of Science, Karad**

**Department of Electronics**

**Feedback form**

Name of the Activity – Student Seminar

Roll No :- 3017

Class : B.Sc III

		Excellent (5)	Very Good (4)	Good (3)	Satisfactory (2)	Poor (1)
1	How would you rate the seminar's organization	✓				
2	Execution of Seminar	✓				
3	Subject interest generated by the departmental staff	✓				
4	Providing the facilities ICT, books, hardware tools etc. during seminar & seminar preparation by department	✓				
5	Support by department during seminar	✓				

Any Other suggestion if any .....

Name of the Students Dhumal Shrawani Kishor

Date : 12/03/2026

Sign : Dhumal



# YASHWANTRAO CHAVAN COLLEGE OF SCIENCE, KARAD

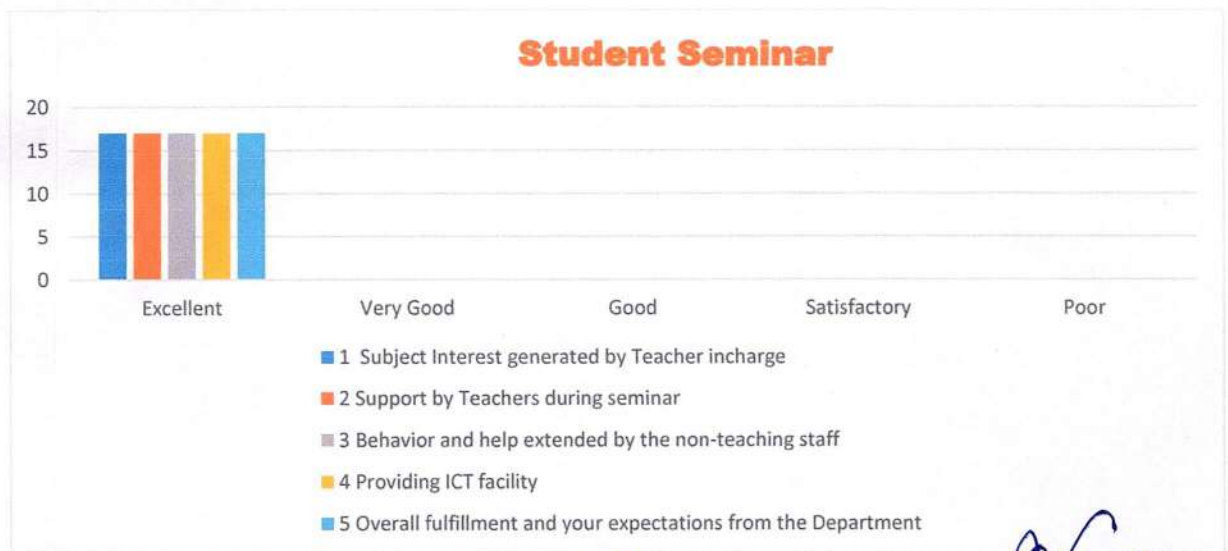
## Department of Electronics

NAME OF THE ACTIVITY: - STUDENT SEMINAR

B.Sc.III

FEEDBACK ANALYSIS - 2025-26

		Excellent	Very Good	Good	Satisfactory	Poor
1	Subject Interest generated by Teacher incharge	17	-	-	-	-
2	Support by Teachers during seminar	17	-	-	-	-
3	Behavior and help extended by the non-teaching staff	17	-	-	-	-
4	Providing ICT facility	17	-	-	-	-
5	Overall fulfillment and your expectations from the Department	17	-	-	-	-



  
Teacher incharge

  
H.O.D.  
HEAD

  
Principal  
Principal

Department of Electronics, Yashwantrao Chavan College of Science, Karad  
Yashwantrao Chavan College of Science, Karad

